

ABSTRACT OF DISCLOSURE

A liquid crystal display includes: a plurality of pixels; a data driver including a memory and a register and supplying data signals to the pixels; and a signal controller supplying a control signal for controlling the data driver to the data driver, 5 wherein the register includes a bit storing a data for determining a frequency of the control signal. The data driver operates in synchronization with the frequency of the control signal. The control signal includes a data enable signal.